

CURRICULUM VITA



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PROFILE (Total Work Experience 31 years in teaching and research)

- Graduated (B.Tech, ECE) from VR Siddartha Engineering College, Vijayawada, in 1983.
- Master of Engineering(Tele Communications Engineering) Jadavpur University, Kolkata, in 1985.
- Ph.D from Andhra University, Visakhapatnam(Array Antenna Design) Jan 2002
- Presently working as a Director R&D, RVR & JC College of Engineering.
- Worked as a Principal, RVR & JC College of Engineering, Guntur, 2010-2016
- Worked as a Professor & Head at the department of ECE, RVR & JC College of Engineering, Guntur, 2000-2010.
- Worked as a lecturer, Assistant Professor and Professor at the department of ECE, RVR & JC College of Engineering, since 1986.
- Worked as a lecturer at KLCE, Vijayawada, in 1985-86.
- Worked as an Associate Professor at AIMST University, Malaysia in 2003-2005.

RESEARCH & SPONSORED GRANTS(Received worth of around Rs 47,82684 from various funding agencies)

- Received Major Research Project grant of Rs 9,02,800/- from UGC, New Delhi for “ Analysis and Measurement of Wide Band Signal Attenuation through Typical Building Materials(On Going).
- Received Major Research Project grant of Rs 11,67,800/- from UGC, New Delhi for “Design of Array Antenna for the generation of Shaped Beams” in the year 2008. (Completed).
- MODROBS Project grant of Rs 13,00,000/- from AICTE, New Delhi in the year 2010.(Completed).
- Received fund of Rs. 6.5 lakhs AICTE MODROBS Project for the development of Microwave and Advanced Communication Engineering Laboratory (completed)

- Received Travel Grant of Rs 1,20,084/- from University Grants Commission, Govt. of India, to present a paper in International Conference on Progress in Electromagnetic Research in Stockholm(SWEDEN) in Aug 2013.
- Received fund of Rs. 6.5 lakhs AICTE MODROBS Project for the development of Microprocessors & Computers Laboratory (completed).
- Received Travel Grant of Rs. 92,000/- to present a paper in International Conference on Progress In Electromagnetic Research in MOSCOW (RUSSIA) supported by UGC, Govt. Of INDIA through Travel Grant Scheme.

PROFESSIONAL SOCIETIES

- ☞ Senior Member of IEEE.
- ☞ Fellow of IETE.
- ☞ Life Member of ISTE
- ☞ Member of SEMCEI
- ☞ Member AMSE

COUNTRIES VISITED

- **MALAYSIA**
- **SINGAPORE**
- **MOSCOW(RUSSIA)**
- **SWEDEN**
- **USA**

ADMINISTRATIVE EXPERIENCE

- Former Member of planning and monitoring board, Acharya Nagarjuna University, Guntur.
- Former Member of Academic Senate, Acharya Nagarjuna University, Guntur.
- Former Dean, Faculty of Engineering, Acharya Nagarjuna University, Guntur
- Worked as Principal of RVR & JC College of Engineering, Guntur during 2010-2016.
- Worked as head of the department of ECE during 2000-2010.
- Chairman of Board of studies for Electronics & Communication Engineering in Acharya Nagarjuna University, Guntur during 2006-2010.
- Chairman of Board of studies for Electronics & Instrumentation Engineering in Acharya Nagarjuna University, Guntur during 2006-2010.
- Member of Board of studies for Electronics department in PB Siddartha College, Vijayawada.
- Member of Board of studies for Electronics department in JKC College, Guntur.
- Worked as convener for various committees at RVR & JC College of Engineering.
- Appointed as Regional Co-ordinator, Guntur Region for EAMCET-2011 Entrance examination conducted by Andhra Pradesh state government.
- Appointed as Panel member in Interview Boards to select Faculty members in various Engineering Colleges.

ACADEMIC ACHIEVEMENTS

- ❖ Conducted National Seminar on VLSI Design funded by A.I.C.T.E, New Delhi.
- ❖ Developed Digital Systems Laboratory worth of around Rs. 20 lakhs for the benefit of ECE department students.
- ❖ Conducted student paper contest ECE MEET-2006.
- ❖ Worked as a faculty member in AIMST university Malaysia and gained International exposure in teaching & research.
- ❖ Co-Authored book on “Circuits & Networks: Analysis and Synthesis”, published by McGraw-Hill publishing Ltd, Golbal Series, USA – 2006.
- ❖ **Co-Authored and Edited Forty Five books published by McGraw-Hill India Publishing Co. Ltd, India.**
- ❖ Published several papers in National / International Journals / conferences.
- ❖ Visited Moscow(Russia) to present a paper in International Conference on Progress In Electromagnetic Research supported by UGC, Govt Of INDIA through Travel Grant Scheme.
- ❖ Visited SWEDEN to present a paper in International Conference on Progress in Electromagnetic Research supported by UGC, Govt. of India through Travel Grant Scheme.

FACULTY DEVELOPMENT PROGRAMMES

- ✓ Organized ISTE-STTP programme on “Advanced Microcomputer Systems” sponsored by AICTE was conducted by department of ECE during August, 1998 for engineering teachers (AICTE funded).
- ✓ Organised AICTE – ISTE – STTP Programme on “Wireless & Mobile Communications” in the department of ECE, R.V.R. & J.C. College of Engineering on July 07-12-2003 for Engineering Teachers (AICTE funded).
- ✓ Participated in entrepreneurship awareness programme on “Service quality Management”, conducted by Entrepreneurship Development Cell of R.V.R. & J.C. College of Engineering on 12-09-1997.
- ✓ Participated in entrepreneurship awareness programme on “Productivity Challenges of 21st Century” on 28th & 29th, August, 1998 in R.V.R. & J.C. College of Engineering.
- ✓ Participated in the National Conference on “Quality Assurance in Technical education organized by VRSEC, Vijayawada on 18-11-2002.
- ✓ Participated international seminar on “Rapid Prototyping, tooling and Manufacturing” organized by faculty of Engineering & Computer Technology, AIMST & PSDC held on 28th June 2004, at Sungai Petani, Malaysia.

RESEARCH PAPERS/ BOOKS PUBLISHED

- 01) **A.Sudhakar**, P. Rama Raju, “Analog Simulation of Catheter – Transducer System”, Modeling, Measurement & Control, C, Vol.32, No. 2, PP. 19-28, 1992, AMSE, FRANCE.
- 02) **A.Sudhakar**, B. Rajan Abraham, I. A. Pasha, V.V.V.S. Prasad, “Microcontroller Based Catheter-Transducer System Control”, Modeling, Measurement & Control, A, Vol. 47, No. 4, 1992, PP. 5-22, AMSE, FRANCE.
- 03) **A.Sudhakar**, I.A. Pasha, D. Madhavi, “A Study on the Bipolar Operation of Field-Effect Transistors”, Modeling, Measurement & Control, Vol. 52, No. 4, PP. 23-38, 1993, AMSE, FRANCE.

- 04) V.V.V.S. Prasad, P.S.M. Satyanarayana, **A.Sudhakar**, "Circuit Techniques for Writing Phase Encoded Data on Magnetic Tape", Modeling, Measurement & Control, A, Vol. 48, No. 2, PP. 1-13, 1993, AMSE, FRANCE.
- 05) I.A. Pasha, **A.Sudhakar**, Y. Mallikarjuna Reddy, "Design Considerations of M-Level staggered PRF and Development of Staggered PRF Unit for MTI Radar using Asynchronous Mod-Counters", Modeling, Measurement & Control, A, Vol. 52, No. 4, PP. 39-48, 1993, AMSE, FRANCE.
- 06) Y. Mallikarjuna Reddy, P.S.M. Satyanarayana, I.A. Pasha, **A.Sudhakar**, "Generalized Differential Encoder for data transmission through Remote Sensing Satellites", Institution of Engineers Journal-ET, Vol. 74, PP. 8-11, May, 1993.
- 07) I.A. Pasha, **A.Sudhakar**, "A Study of the Effect of Multi Path Raleigh Fading Channel on coherent PSK Communication System", Advances in Modeling & Analysis, B, Vol. 29, No. 2, PP. 41-50, 1994, AMSE, FRANCE.
- 08) **A.Sudhakar**, I.A. Pasha, "Design consideration of Minimum Phase FIR Filters", Advances in Modeling & Analysis, B, Vol. 35, No. 3, PP. 1-10, 1995, AMSE, FRANCE.
- 09) **A.Sudhakar**, "Low Cost Microprocessor based open loop controlled system", Modeling, Measurement & Control, B, Vol. 61, No. 1, PP. 47-58, 1995, AMSE, FRANCE.
- 10) I.A. Pasha, **A.Sudhakar**, "Staggered PRF MTI System performance analysis", Modeling, Measurement & Control, A, Vol. 66, No. 3, PP. 1-16, 1995, AMSE, FRANCE.
- 11) **A.Sudhakar**, "Digital Image Processing of remotely sensed data for enhancing the geological features", Advances in Modeling & Analysis, B, Vol. 35, PP. 11-18, 1995, AMSE, FRANCE.
- 12) **A.Sudhakar**, G.S.N. Raju, K.R. Gottumukkala, "Generation of ramp type of radiation Patterns from an array antennas", National Journal of EMC, Vol. II, Nos. 1 & 2, PP. 5-12, April & October 1998.
- 13) **A.Sudhakar**, G.S.N.Raju, "Generation of Ramp Patterns using Amplitude Control", Proc. of Fact-2k, PP-43-48, February 2000, Andhra University, Visakhapatnam.
- 14) **A.Sudhakar**, P. Saritha, M. Dilip Chakravarthy, G.S.N. Raju, K.R. Gottumukkala, "Optimized Ramp Ppterns", Modeling, Measurement & Control, Vol. 73, No. 1, 2, PP. 1-8, 2000, AMSE, FRANCE.
- 15) K.Srinivasa Rao, **A. Sudhakar**, Y.V.Narayana, G.S.N.Raju, "Generation of Optimized Shaped Beams", Proc. Of SAT-03, PP-5, August 2003, Andhra University, Visakhapatnam – 2003.
- 16) K.V.R.Chowdary, N.Renuka, **A.Sudhakar**, G.S.N.Raju, "Realization of Ramp patterns using phase only control technique", Proc. of ACT-06, PP. 95-97, 8th & 9th December – 2006, Andhra University, Visakhapatnam.
- 17) **A.Sudhakar**, G.S.N. Raju, K.R. Gottumukkala, "Generation of Stair-Step patterns from the line source", Modeling Measurement & Control, Vol. 47, No.6, PP 17-26, 2004, AMSE, FRANCE.
- 18) **A.Sudhakar**, A. Kalyan Venugopal, P. Sreekanth, G.S.N. Raju, K.R. Gottumukkala, "Generation of Stair-Step radiation Patterns from an Array Antenna", Modeling, Measurement & Control, Vol. 74, No. 2, PP. 7-16, 2001, AMSE, FRANCE.
- 19) N.Renuka, **A.Sudhakar**, G.S.N.Raju, B.Prabhakara Rao, "Generation of stair Step Patterns from discrete arrays of radiating elements", Proc. of ACT-06, PP. 65-67, 8th & 9th December – 2006, Andhra University, Visakhapatnam.
- 20) **A.Sudhakar**, PSM Satyanarayana, "Circuit and Networks Analysis and Synthesis", 1st Edition, published by Tata McGraw-Hill Education (India) Private Limited, 1994, New Delhi, India
- 21) **A.SUDHAKAR**, PSM Satyanarayana, "Network Theory" published by Tata McGraw-Hill Education (India) Private Limited, 2002, New Delhi, India.
- 22) **A.Sudhakar**, PSM Satyanarayana, "Networks Analysis", published by Tata McGraw-Hill Education (India) Private Limited, ISBN: 0-07-053315-9, 2003, New Delhi, India.

- 23) **A.SUDHAKAR**, PSM Satyanarayana, “Electrical Circuit Analysis ” published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 978-0-07-626226-3, New Delhi, India.
- 24) **A.SUDHAKAR**, PSM Satyanarayana, “Network Theory”, 2nd Edition, published by Tata McGraw-Hill Education (India) Private Limited, 2004, New Delhi, India.
- 25) **A.SUDHAKAR**, PSM Satyanarayana, “Electrical Circuit”, 2nd Edition, published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 0-07-063447-5, New Delhi, India.
- 26) **A.SUDHAKAR**, PSM Satyanarayana, “Network Analysis”, 2nd Edition, published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 0-07-063446-7, New Delhi, India.
- 27) **A.SUDHAKAR**, PSM Satyanarayana, “Electrical Circuit”, 3rd Edition, published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 978-0-07-066055-7, New Delhi, India.
- 28) **A.SUDHAKAR**, PSM Satyanarayana, “ Circuit and Networks Analysis and Synthesis”, 2nd Edition, published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 0-07-048295-6, 2006, New Delhi, India.
- 29) **A.SUDHAKAR**, PSM Satyanarayana, “Networks Analysis”, 3rd Edition, published by Tata McGraw-Hill Education (India) Private Limited, ISBN(13): 978-0-07-066111-0, August 2007, New Delhi, India.
- 30) **A.Sudhakar**, P.S.M. Satyanarayana, “Circuits & Networks: Analysis and Synthesis”, 3rd Edition, Published by Tata McGraw – Hill publishing Co. Ltd. 2007, India.
- 31) **A.Sudhakar**, P.S.M. Satyanarayana, “Network and Systems”, Published by Tata McGraw – Hill publishing Co. Ltd. 2002, India.
- 32) **A.Sudhakar**, P.S.M. Satyanarayana, “Network Theory”, Published by Tata McGraw – Hill publishing Co. Ltd. 2003, India.
- 33) **A.Sudhakar**, P.S.M. Satyanarayana, M. Ravindra Reddy, “Signals & Systems”, Published by Tata McGraw – Hill publishing Co. Ltd. 2003, India.
- 34) **A.Sudhakar**, P.S.M. Satyanarayana, M. Ravindra Reddy, “Signals & Systems”, Published by Tata McGraw – Hill publishing Co. Ltd. 2004, India.
- 35) **A.Sudhakar**, P.S.M. Satyanarayana, “Network Analysis and Synthesis”, Published by Tata McGraw – Hill publishing Co. Ltd. 2004, India.
- 36) **A.Sudhakar**, P.S.M. Satyanarayana, “Electrical Circuits”, Published by Tata McGraw – Hill publishing Co. Ltd. 2005, India.
- 37) **A.Sudhakar**, P.S.M. Satyanarayana, “Network Analysis”, Published by Tata McGraw – Hill publishing Co. Ltd. 2005, India.
- 38) **A.Sudhakar**, P.S.M. Satyanarayana, “Circuits and Network”, by McGraw-Hill Publishing Co., USA – 2006.
- 39) **A.Sudhakar**, P.S.M. Satyanarayana, “ Network Analysis, 4th Edition”, by McGraw-Hill India publishing Ltd.2008, India.
- 40) **A.Sudhakar**, P.S.M. Satyanarayana, “ Electrical Circuit Analysis”, by McGraw-Hill India Publishing Ltd.2008, India.
- 41) C.Subba Rao, J. Ravindranadh, **A.Sudhakar**, “ Design and modeling of corner reflector antenna for S-band frequencies”, Proc. of NSACT, pp. 35-39, 30th Jan-2009, VRSEC, Vijayawada, India.
- 42) J.Ravindranadh, C.Subba Rao, **A.Sudhakar**, “Investigation on effect of height over an antenna performance”, Proc. of NSACT, pp. 7-9, 30th Jan-2009, VRSEC, Vijayawada, India.
- 43) KVR Chowdary, TSRK Prasad, **A.Sudhakar**, “Modeling of small dipole antenna for UWB applications”, Proc. of NSACT, pp. 17-19, 30th Jan -2009, VRSEC, Vijayawada, India.
- 44) R.Divyasri, M.Rajani, M.Hema Sirisha, **A.Sudhakar**, “ Modeling and analysis of corner reflector antenna”, Proc. of ICET-09, pp.21-23,27-28th Feb,2009, VCET, Madhurai, India.

- 45) C.Subbarao, J.Ravindranadh, **A.Sudhakar**, “ Design of line source antennas for generation of ramp patterns”, Proc. of ICET-09, pp. 26-27, 27-28th Feb-2009, VCET, Madhurai, India.
- 46) J.Ravindranadh, C.Subbarao, **A.Sudhakar**, “ Design of amplitude distribution function for desired stair-step patterns”, Proc. of an International conference on Advances in optoelectronics, Information and Communication Technologies (ICOICT)-2009, pp. 436-438, 26-27 Feb-2009, Trivandrum, India.
- 47) C.Subbarao and **A.Sudhakar**, “ Modeling and Analysis of Yagi-Uda Antenna with and without Radome”, Published in the Proceedings of National Conference on Microelectronics and Communication, April 8-9, ICET-2009, SRM University, Chennai, India.
- 48) **A.Sudhakar** and Y.V. Narayana, “ Realization of Ramp and Stair-step Patterns from the Rectangular Wave-guide Arrays” Published in the Proceedings of International Conference on Progress In Electromagnetics Research Symposium, pp. 418-421, August 18-21, 2009, Moscow Institute of Radio Engineering Electronics and Automatics, Moscow, Russia.
- 49) Y.V.Narayana, **A.Sudhakar**, G.S.N.Raju, “ Generation of sector beams from array of wire antennas”, Advances in modeling, series B, volume 53, issue 2, pp 74-84, 2009, AMSE, FRANCE.
- 50) J.Ravindranadh, **A.Sudhakar**, K.Padma Raju, “ Optimization of ground reflectors for high directive helical antenna”, Published in the Proceedings of International Conference on Modeling and Simulation (MS09), pp. 301-303, Dec 1-3, 2009, Organized by College of Engineering(Trivandrum), AMSE (France) and Department of Technical Education, Govt. of Kerala.
- 51) C.Subba Rao, **A.Sudhakar**, “Design and Modeling of Biconical Dipole for Broad Band Applications”, Published in the Proceedings of International Conference on Advances in Computing, Control, and Telecommunication Technologies, pp. 248-251, Dec 28-29,2009 in Trivandrum, Kerala, India. Published in IEEE Xplore digital library 978-0-7695-3915-7/09 © 2009 IEEE DOI 10.1109/ACT.2009.69.
- 52) B.Saidaiah, **A.Sudhakar**, K.Padma Raju, “Modeling and Analysis of an Efficient Bow- Tie Antenna for UWB Applications”, Published in the Proceedings of International Conference on Advances in Computing, Control, and Telecommunication Technologies, pp. 376-379, Dec 28-29,2009 in Trivandrum, Kerala, India. Published in IEEE Xplore digital library 978-0-7695-3915-7/09 © 2009 IEEE DOI 10.1109/ACT.2009.99.
- 53) **A.Sudhakar**, PSM Satyanarayana, “Network Analysis, Fifth Edition”, Published by Tata McGraw-Hill Education Private Limited, ISBN: 978-0-07-068126-2, August 2009, New Delhi, INDIA.
- 54) **A.Sudhakar**, PSM Satyanarayana, “ Electrical Circuit Analysis, Second Edition”, Published by Tata McGraw-Hill Education Private Limited, ISBN:978- 0-07-0681123-1, August 2009, New Delhi, INDIA.
- 55) T.Venkateswara Rao, **A.Sudhakar**, K. Padma Raju, “ Effects of Radiation Parameters on UWB Antenna Design”, Published in the proceedings of National Conference on Signal Processing & Communication Systems, NCSPCS 2010,pp 237- 240, Feb 25-26, 2010, organized by RVR & JC College of Engineering, Guntur-19, AP, INDIA.
- 56) J. Ravindranadh, **A.Sudhakar**, K. Padma Raju, “ Design of Moderately Wideband Circularly Polarized Helical Antenna”, Published in the proceedings of National Conference on Signal Processing & Communication Systems, NCSPCS 2010,pp 237- 240, Feb 25-26, 2010, organized by RVR & JC College of Engineering, Guntur-19, AP, INDIA.
- 57) Saidaiah Bandi, **A.Sudhakar**, K.Padma Raju “ Design of CPW-fed Planar Trapezoidal Monopole”, Published in the proceedings of National Conference on Signal Processing & Communication Systems, NCSPCS 2010,pp 237- 240, Feb 25-26, 2010, organized by RVR & JC College of Engineering, Guntur-19, AP, INDIA.
- 58) C.Subba Rao, **A.Sudhakar**, “Analysis of Square Loop Antenna for Broad Band Applications”, the proceedings of National Conference on Signal Processing &

- Communication Systems, NCSPCS 2010, pp 237- 240, Feb 25-26, 2010, organized by RVR & JC College of Engineering, Guntur-19, AP, INDIA.
- 59) C.Subba Rao, **A.Sudhakar**, “High gain ultra wideband biconical antenna array”, published in the International Journal AMSE, Modelling, Measurement & Control, Series A, Vol 83, issue 1, pp 68-78, 27th Feb 2010, FRANCE.
 - 60) Saidaiah Bandi, **A.Sudhakar**, K.Padma Raju, “ Modeling and Analysis of CPW-fed Planar Trapezoidal Monopole Antenna for Mobile Communication Applications” ANU Journal of Engineering & Technology, Volume 1, No1, PP. 34-38, June 2009, India.
 - 61) C.Subba Rao, **A.Sudhakar**, “Modeling and Analysis of Electrically Large Square Loop Antenna” ANU Journal of Engineering & Technology, Volume 1, No1, PP. 31-33, June 2009, India.
 - 62) J.Ravindranadh, **A.Sudhakar**, K.Padma Raju, Analysis of Circularly Polarized L-band Helical Antenna” ANU Journal of Engineering & Technology, Volume 1, No2, PP. 48-51, December 2009, India.
 - 63) **A.Sudhakar**, PSM Satyanarayana, “Circuits and Networks: Analysis and Synthesis” Fourth Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-069972-4, June 2010, New Delhi, INDIA.
 - 64) **A.Sudhakar**, PSM Satyanarayana, “Electric Circuits” First Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-070268-4, July 2010, New Delhi, INDIA.
 - 65) **A.Sudhakar**, PSM Satyanarayana, “Electric Circuits” First Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-070473-2, July 2010, New Delhi, INDIA.
 - 66) **A.Sudhakar**, PSM Satyanarayana, “Principles of Electrical Engineering” JNTUH edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978-0-07-107287-8, Jan 2011, New Delhi, INDIA.
 - 67) **A.Sudhakar**, PSM Satyanarayana, “Network Theory” JNTUH Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-107288-5, Jan, 2011, New Delhi, INDIA.
 - 68) **A.Sudhakar**, PSM Satyanarayana, “Principles of Electrical Engineering” JNTUA Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-107291-5, Jan 2011, New Delhi, INDIA.
 - 69) **A.Sudhakar**, PSM Satyanarayana, “Network Theory” JNTUA Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-107292-2, Jan 2011, New Delhi, INDIA.
 - 70) Subba Rao, C; **A. Sudhakar**; “Analysis of linear biconical antenna array”, Published in IEEE X-plore, IEEE international conference on Communication Control and Computing Technologies (ICCCCT), 2010, pp 12-16, 7-9 OCT 2010, ISBN: 978-1-4244-7769-2, St.Xaviers Catholic College of Engineering, Chankankadai, Nagercoil, Tamil Nadu, India.
 - 71) M.Sunitha, C. Subba Rao, **A.Sudhakar**, K.Veeraraj, “ Generation of shaped beams by using amplitude control method”, published in proceedings of International conference on Next Generation Communication and Computing Systems (ICNGCCS-10), organized by Institution of Engineers, Technocrats and Academicians Network, pp 136-138, December 25-26, 2010, Chandigarh, India.
 - 72) C. Subba Rao, **A.Sudhakar**, “ Biconical Linear Array Analysis For Non-Uniform Amplitude Excitation Methods”, Published in ARPN International Journal of Engineering and Applied Sciences, pp 1-7, Vol 6, No-4, APRIL 2011, ISSN 1819-6608.
 - 73) J. Ravindranadh, **A.Sudhakar**, K.Padma Raju, “ Design of optimized reflector for enhancing the gain of helical antenna” International Journal of Electronics & Telecommunication and Instrumentation Engineering (IJETIE), Vol 4, issue 1, pp 23-26, Sep 2010, ISSN 0974-4975.

- 74) **A.Sudhakar**, PSM Satyanarayana, "Electric Circuits" JNTUH Second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-132891-3, June 2011, New Delhi, INDIA.
- 75) **A.Sudhakar**, PSM Satyanarayana, "Electrical Circuits" JNTUA Second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-132888-3, June 2011, New Delhi, INDIA.
- 76) **A.Sudhakar**, PSM Satyanarayana, "Electrical Circuit Analysis-I" JNTU, Kakinada Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-132999-6, June 2011, New Delhi, INDIA.
- 77) **A.Sudhakar**, PSM Satyanarayana, "Network Analysis" JNTU, Kakinada Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 0-07-132873-9, June 2011, New Delhi, INDIA.
- 78) D. Madhavi, G. Padmaja Rani, N.V. Purnachandra Rao, **A. Sudhakar**," Modeling of Monopole Microstrip Patch Antenna for UWB Applications", Published in ANU Journal of Engineering & Technology, No-1, Vol 2, pp 69-72, June-2010.
- 79) C. Subba Rao, **A. Sudhakar**," Generation of Sector Beam with Biconical Linear Antenna Array", Published in ANU Journal of Engineering & Technology, No-2, Vol 2, pp 1-4, June-2010.
- 80) **A.Sudhakar**, PSM Satyanarayana, "Network Theory, 2e" JNTU, Hyderabad second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN(13):978-1-25-900448-3, November 2011, New Delhi, INDIA.
- 81) **A.Sudhakar**, PSM Satyanarayana, "Principles of Electrical Engineering, 2e" JNTU, Hyderabad Second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN(13):978-1-25-900449-0, November 2011, New Delhi, INDIA.
- 82) **A.Sudhakar**, PSM Satyanarayana, "Network Theory, 2e" JNTU, Ananthapur, Second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN(13): 978-1-25-900452-0, November 2011, New Delhi, INDIA.
- 83) **A.Sudhakar**, PSM Satyanarayana, "Principles of Electrical Engineering, 2e" JNTU, Ananthapur Second Edition Published by Tata McGraw-Hill Education Private Limited, ISBN: 978- 1-25-900453-7, November 2011, New Delhi, INDIA.
- 84) **A.Sudhakar**, PSM Satyanarayana, "Electrical Circuit Analysis-2" JNTU, Kakinada Edition Published by Tata McGraw-Hill Education Private Limited, ISBN(13): 978-1-25-900454-4, November 2011, New Delhi, INDIA.
- 85) C. Subba Rao, **A. Sudhakar**, " Biconical Ring Antenna Array for Wide Band Applications" International Journal of Engineering Science and Technology (IJEST), No 2, Vol 4, PP 759-763, Feb-2012, ISSN:0975-5462.
- 86) **A.Sudhakar**, PSM Satyanarayana, "Electrical Circuit Analysis-I, 2E" JNTUK-2012, Published by Tata McGraw-Hill Education Private Limited, ISBN(13): 978-1-25-900669-2, Aug 2012, New Delhi, INDIA.
- 87) **A.Sudhakar**, PSM Satyanarayana, "Network Analysis, 2E" JNTUK-2012, Published by Tata McGraw-Hill Education Private Limited, ISBN(13): 978-1-25-900667-8, Aug 2012, New Delhi, INDIA.
- 88) B. Saidaiah, **A.Sudhakar**, K. Padma Raju, " Circular Disk Monopole Antenna for Broad Band Applications", International Journal of Scientific and Research Publications, Volume 2, Issue 6, June 2012, ISSN 2250-3153.
- 89) **A.SUDHAKAR**, PSM Satyanarayana, " Electrical Circuits, 3E" JNTUA 2012, Published by Tata McGraw-Hill Private Limited, ISBN(13):978-1-25-902889-2, Nov 2012, New Delhi, India.
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AWARDS

- **TEACHING AWARD:** An incentive of Rs. 5000/- along with citation was given by the Management of R.V.R. & J.C.C.E for authoring a text book published by TMH publishing co. Ltd, New Delhi.



- **Received Prathibha Puraskar** from TR Mahesh Charitable Trust, Guntur, for services rendered in the field of Higher Education, Andhra Pradesh on 19th July, 2014.



- Received Certificate of Excellence for outstanding services, achievements, and contributions to the field of Electronics and Communication Engineering by the IETE Vijayawada centre on the occasion of IETE Foundation Day on 2nd Nov, 2014.



- As Head of the Institution, Received the Prestigious Indira Gandhi National Service Scheme Award 2013-14 from the President of India for the NSS Unit of RVR & JC College of Engineering.



- Received Outstanding Teacher Award for committed and dedicated efforts in influencing the minds and personalities of students to face the challenges of future successfully and preparing the next generation by Rotary Club, Guntur on 05-09-2015.

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